

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First-Named Inventor: PAYNE, Robert

Application No.: 09/997,609 Conf.: 7795

Date Filed: 11/29/2001

Docket No.: US01 8186

Art Unit: 2661

Examiner: Unknown

Title: High-Speed Interchip Interface Protocol

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

APPOINTMENT OF ASSOCIATES

Sir:

The undersigned Attorney of Record hereby revokes all prior appointments (if any) of Associate Attorney(s) or Agent(s) in the above-captioned case and appoints:

Chris Horgan	Registration No. 40,394;
Edward Blocker	Registration No. 30,245;
Kevin Fortin	Registration No. 35,140;
Kevin Simons	Registration No. 45,110;
Michael J. Ure	Registration No. 33,089; and
Peter Zawilski	Registration No. 43,305

c/o PHILIPS ELECTRONICS NORTH AMERICA CORPORATION, Corporate Intellectual Property, 1000 West Maude Ave., Sunnyvale, California 94085, his Associate Attorneys/Agents with all the usual powers to prosecute the above-identified application and any division or continuation thereof, to make alterations and amendments therein, and to transact all business in the Patent and Trademark Office connected therewith.

All correspondence concerning this application and the letters patent when granted should be addressed to the undersigned Attorney of Record.

Respectfully,

By


Michael E. Schmitt, Reg. 36,921

Attorney of Record

Dated at Sunnyvale, California
on August 1, 2003.